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A subthreshold CMOS circuit for a piecewise linear neuromorphic oscillator with current-mode low-pass filters

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Abstract

We propose an analog current-mode subthreshold CMOS circuit implementing a piecewise linear neuromorphic oscillator. Our circuit was derived from a piecewise linear oscillator model proposed by Matsuoka, well known as a building block for constructing a robot locomotion controller. We modified Matsuoka's oscillator to be suitable for analog current-mode integrated circuit implementation, and designed and fabricated it as an analog current-mode circuit. Through circuit simulations and experimental results on a fabricated chip, we demonstrate that our neuromorphic oscillator generates a stable oscillation, and the amplitude and frequency of the oscillation can be controlled by tuning bias currents over a wide range. Further, we propose a compensation for device mismatch in the neuromorphic oscillator through feedback from a coupled physical system.

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1. Introduction

Fundamental rhythmic movements for locomotor behavior of animals, such as walking, running, flying, and swimming, are generated by part of the central nervous system called the central pattern generator (CPG) [8]. Induced by inputs from a higher level, a CPG generates rhythmic neural activity activating muscles in the absence of any sensory inputs, resulting in locomotor behavior of animals. While not necessary for generating rhythmic activity, sensory inputs regulate such rhythmic activity over a wide range. As a result, locomotor behavior of animals can be adapted to unpredictable environments [12].

From a point of view of nonlinear dynamics, it is explained that rhythmic movements during locomotion emerge as a stable limit cycle from mutual entrainment between the neural system that includes the CPG and the

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For utilizing *global entrainment* to control locomotor behavior of robots, many researchers have dedicated efforts to designing locomotion controllers based on CPG (e.g., [3,11,13,14,16,20,24,25,30,31,33]). Taga et al. have used a CPG model constructed from the neural oscillator model proposed by Matsuoka [17] in simulating for biped locomotion [31]. Kimura et al. have used a CPG model for quadruped robot locomotion on rough terrain [13]. Williamson has applied a CPG model for controlling rhythmic arm movements of a humanoid robot [33].

Such CPG models consist of coupled nonlinear oscillators, each of which generates rhythmic activity for actuating each joint of the limbs. Functions of a CPG model depend on both dynamical properties of a nonlinear oscillator as a component and its coupling topology. Collins et al. [7] and Golubitsky [9] have shown common properties of CPG models consisting of different types of nonlinear oscillators, e.g., symmetry-breaking bifurcation [9], that only depends on their coupling topology. However, recent

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findings suggest that nonlinear phenomena, such as flexible phase-locking [25] and phase resetting [24], play key roles in utilizing sensory feedback effectively for high adaptation during locomotion. These phenomena depend on intrinsic properties of nonlinear oscillators underlying a CPG model.

In neuromorphic engineering, many CPG models have been implemented into silicon chips [2,5,15,22,23,26–29,32]. As a building block for a CPG chip, using a neuromorphic oscillator to control the amplitude and frequency of the oscillation over a wide range is desirable because entrainment properties of CPG chips rely on a dynamic range of a neuromorphic oscillator, and such entrainment properties are also significant for utilizing sensor feedback effectively.

The aim of this work is to implement a neuromorphic oscillator with high controllability of the amplitude and frequency of oscillation. We focused on the piecewise linear oscillator model proposed by Matsuoka that provides scalability of the amplitude of oscillation [17] because such scalability has an advantage for reflecting sensory feedback in rhythmic pattern generation [13,30,31]. For using the scalability effectively, we revised the model slightly, and implemented it as an analog subthreshold CMOS circuit using current-mode representation with a wide dynamic range [1]. Through SPICE simulations and experiments on a fabricated chip, we demonstrated that our circuit can generate a stable oscillation of currents, and the amplitude and frequency of the oscillation can be controlled over a wide range by tuning bias currents. Further, we propose a method to compensate device mismatch effects in a neuromorphic oscillator exploiting sensory feedback from a coupled physical system.

The present paper is organized as follows. In Section 2, we introduce a piecewise linear oscillator model suitable for analog current-mode integrated circuit implementation. In Section 3, we describe an analog current-mode circuit for the piecewise linear oscillator model. We demonstrated the performance of our neuromorphic oscillator through circuit simulations and experiments that we describe in Section 4. We also describe a method to compensate device mismatch effects exploiting physical feedback in Section 5. A summary of this paper is presented in Section 6.

2. Piecewise linear oscillator models

Let us here describe two piecewise linear oscillator models based on the concept of the half-center oscillator in the field of neuroscience. One has been frequently used as a component of a CPG-based controller in robotics. Another is a version of the previous model revised to be suitable for analog integrated circuit implementation by using currentmode representation.

2.1. Biological concept of half-center oscillator

We here briefly review the biological concept of the halfcenter oscillator to account for alternating rhythmic activity in flexor and extensor motoneurons in locomotion



Fig. 1. Conceptual illustration of half-center oscillator.

of animals [6]. Fig. 1 shows the half-center oscillator model proposed by Brown, which consists of two neurons; a flexor half-center and an extensor half-center, each is connected with reciprocal inhibition. The half-centers alternatively activate flexor and extensor motoneurons in the absence of any pacemaker cells. Each half-center has dynamical properties, such as self-inhibition, fatigue or adaptation. The flexor half-center activates the flexor muscles and suppresses the extensor half-center via synaptic inhibition in the flexion phase; in turn, transition from the flexion phase to extension phase occurs due to the self-inhibition and adaptation. As in the case of the flexion phase, the extensor half-center activates the extensor muscles and suppresses the flexor half-center in the extension phase.

2.2. Half-center oscillator model with piecewise linearity

Matsuoka proposed a half-center oscillator model consisting of two neurons with piecewise linearity. The dynamics of the half-center oscillator model are described by the following system equations [17]:

$$\tau_u \frac{\mathrm{d}u_i}{\mathrm{d}t} = -u_i + s - \beta v_i - w_{ij} f(u_j),\tag{1}$$

$$\tau_v \frac{\mathrm{d}v_i}{\mathrm{d}t} = -v_i + f(u_i),\tag{2}$$

where u_i represents the inner state of the *i*th neuron, v_i an adaptation variable of the *i*th neuron (i = 1, 2), s a tonic input, w_{ij} a synaptic strength between the *i*th and *j*th neuron, β the adaptation effectiveness, τ_u a time constant of the self-inhibition, and τ_v a time constant of the adaptation effect. The nonlinearity of this model is given in the form of a piecewise linear function

$$f(x) = \max(0, x),\tag{3}$$

where f corresponds to the output of a neuron. Depending on the parameters, this model has a stable limit-cycle oscillation. The stability and dynamical properties of this model are analyzed in detail [4,10,17,18]. The amplitude of the oscillation is proportional to the tonic input s due to the piecewise linearity, in other words, the amplitude of the oscillation is scalable. The frequency and shape of the oscillation can also be controlled by tuning the ratio of time constants. These properties are well suited for controlling rhythmic movements of a robot. In particular, the scalability of the amplitude of the oscillation is appropriate for utilizing sensory feedback [31] that plays critical roles in adapting rhythmic movements to unexpected environments [8]. Focusing on such properties, this model has been fluently used in robotics [13,30,31]. Taga et al. have used it in simulating biped locomotion [31]. Kimura et al. have applied it to control a quadruped walking robot on rough terrain [13]. Williamson has applied it to control robot arm movements [33].

Fig. 2A shows closed (u_1, v_1) phase plane portraits of Matsuoka's model for different tonic inputs, s = 0.5, 1.5, and 2.5, where we set the parameters as follows: $\beta = 3.5$, $w_{ij} = 2.5$, and $\tau_u = \tau_v = 2.5$. We confirmed that the amplitude of the oscillations were proportional to the tonic inputs.

2.3. Piecewise linear half-center oscillator model for circuit implementation

We here introduce a piecewise linear half-center oscillator model for analog current-mode circuit implementation. For effectively utilizing the scalability of the amplitude of oscillation of Matsuoka's model, we consider implementing it into a silicon chip by using current-mode representation with a wide dynamic range. However, it should be noted that the state variables of Matsuoka's model, u_i and v_i , can be both positive and negative, as shown in Fig. 2A. For simplicity, such state variables are desirable not to have polarity because to represent a state variable with polarity using uni-directional currents is complex. To avoid complexity in current-mode circuit implementation, we revised Matsuoka's model slightly as follows:

$$\tau_u \frac{\mathrm{d}u_i}{\mathrm{d}t} = -u_i + f(s - \beta v_i - w_{ij}u_j),\tag{4}$$

$$\tau_v \frac{\mathrm{d}v_i}{\mathrm{d}t} = -v_i + f(u_i),\tag{5}$$

where all variables and parameters are same as in (1)–(2), and the nonlinear function f is the same as the piecewise linear function given by (3). In this model, when u_i becomes very close to 0, the derivative of u_i with regard to t turns to positive. As a result, we can obtain a limit-cycle solution such that all state variables are positive. Fig. 2B shows closed (u_1, v_1) phase plane portraits of the revised model for different tonic inputs, s = 0.5, 1.5, and 2.5, where we set the parameters as follows: $\beta = 5, w_{ij} = 4, \text{ and } \tau_u = \tau_v = 2.5$. Thus, this model is suitable to be implemented as an analog current-mode circuit that treats uni-directional currents.

3. Circuit implementation

We here propose an analog current-mode integrated circuit for the piecewise linear half-center oscillator model described in the previous section.

Fig. 3 is a block diagram of the half-center oscillator model that consists of four low-pass filters and four piecewise linear functions. The low-pass filters can be implemented with a current-mode low-pass filter operating in log-domain based on the dynamic translinear principle [19,21]. Fig. 4 is a schematic of the current-mode low-pass filter. The circuit dynamics are expressed by the following



Fig. 3. Block diagram of the revised piecewise linear half-center oscillator model.



Fig. 2. Phase plane portraits of (A) the half-center oscillator model proposed by Matsuoka and (B) a revised version for analog current-mode circuit implementation.

equation:

$$\tau \frac{\mathrm{d}I_{\mathrm{out}}}{\mathrm{d}t} = -I_{\mathrm{out}} + I_{\mathrm{in}},\tag{6}$$

where I_{in} represents the input current, I_{out} the output current, and τ the time constant, which is expressed by

$$\tau = \frac{CU_{\rm T}}{I_{\tau}},\tag{7}$$

where C represents the capacitance, U_T the thermal voltage, and I_{τ} the bias current. The low-pass characteristics with respect to the output current I_{out} are derived from the currents relationship as a result of a translinear



Fig. 4. Schematic of the current-mode low-pass filters.

loop formed by M1–M4 and dynamic translinear circuits including M2 and M3 with the capacitance C, which is descried in detail [19]. The piecewise linear function (3) can be directly implemented with a current mirror.

Fig. 5 is a schematic of a piecewise linear half-center oscillator circuit (hereafter called piecewise linear neuro-morphic oscillator) consisting of four current-mode low-pass filters and several current mirrors. The dynamics of the circuit are expressed by the following equations:

$$\tau \frac{dI_{u_i}}{dt} = -I_{u_i} + f(I_s - \beta I_{v_i} - w I_{u_j}),$$
(8)

$$\tau \frac{\mathrm{d}I_{v_i}}{\mathrm{d}t} = -I_{v_i} + f(I_{u_i}),\tag{9}$$

where I_{u_i} corresponds to the inner state of the *i*th neuron, I_{v_i} an adaptation variable of the neuron, I_s a tonic input, w_{ij} a synaptic strength between the *i*th and *j*th neuron, and β the adaptation effectiveness. The parameters w_{ij} and β are determined by the current transfer ratio of the current mirrors. The time constant τ can be controlled by tuning the bias current I_{τ} as described in (7). Depending on these circuit parameters, this circuit generates a stable limit-cycle oscillation of the currents I_{u_i} and I_{v_i} corresponding to the state variables u_i and v_i in (4) and (5). Thus, the dynamics of the piecewise linear neuromorphic oscillator are qualitatively the same as that of the piecewise linear halfcenter oscillator model described by (3)–(5).



Fig. 5. Schematic of the piecewise linear half-center oscillator circuit.

4. Results

We describe the performance of the piecewise linear neuromorphic oscillator through circuit simulations and experiments on a fabricated chip.

4.1. Simulation results

We simulated the proposed circuit with HSPICE using BSIM 3v3 LEVEL 49 model parameters for AMIS CMOS 1.5- μ m process. We set circuit parameters as follows: the capacitance of the current-mode low-pass filters C = 10 nF, and the power-supply voltages VDD = 1.5 V and Vref = 0.35 V. The gate length L of transistors were set at $L = 6.0 \mu$ m and the gate width W of the minimum-size transistor was set at $W = 4.5 \mu$ m

4.1.1. Rhythmic pattern generation

Here, we describe rhythmic pattern generation in the piecewise linear neuromorphic oscillator. Fig. 6A presents the waveforms of the state currents I_{u_i} and I_{v_i} , where the parameters $\beta = 5$ and $w_{ij} = 4$, and the bias currents were set at $I_{\tau} = 10$ nA and $I_s = 100$ nA. The equilibrium currents of the circuit are calculated by solving the

following equations:

$$\frac{dI_{u_i}}{dt} = \frac{dI_{v_i}}{dt} = 0 \quad (i = 1, 2)$$
(10)

that yield

$$I_{u_0} = I_s - \beta I_{v_0} - w_{ij} I_{u_0}, \quad I_{v_0} = I_{u_0}, \tag{11}$$

where I_{u_0} and I_{v_0} represent the equilibrium currents. In this simulation, the equilibrium currents were $I_{u_0} = I_{v_0} = I_s/10 = 10 \text{ nA}$. Fig. 6B shows a closed (I_{u_1}, I_{v_1}) phase plane portrait of the circuit. These results confirmed that the circuit generated a stable oscillation.

4.1.2. Frequency and amplitude modulation

The amplitude of the oscillation was proportional to the bias currents I_s as a result of the scaling of the currents I_{u_i} and I_{v_i} due to I_s . We changed the amplitude of the oscillation by tuning I_s from 10 nA at 0 s to 100 nA at 1000 ms as shown in Fig. 6C. We also changed the frequency of the oscillation by tuning the bias current I_{τ} from 10 nA at 0 s to 50 nA at 1000 ms as shown in Fig. 6D. The controllability of the amplitude and frequency of the oscillation are suitable for a neuromorphic oscillator as a building block for constructing a CPG-based controller.



Fig. 6. Simulation results: (A) waveforms of currents and (B) phase plane portrait of the piecewise linear neuromorphic oscillator. (C) Amplitude and (D) frequency modulation by tuning bias currents.

4.2. Experimental results

We designed and fabricated a prototype chip of the proposed circuit with a scalable CMOS rule: MOSIS AMI, *n*-well double-poly double-metal CMOS process, $\lambda = 0.8 \,\mu\text{m}$ and feature size: 1.5- μ m (Fig. 7). Fig. 8 is a micrograph of the piecewise linear neuromorphic oscillator containing four current-mode low-pass filters and several current-mirrors (chip size: $2.25 \times 2.25 \,\text{mm}^2$). We set the gate length of the transistors $L = 9.6 \,\mu\text{m}$. The parameters $\beta = 5$ and $w_{ij} = 4$ were determined by the current transfer ratio of the current mirrors at layout. For measurement, we used the off-chip capacitance $C = 1 \,\mu\text{F}$ and the supply voltages VDD = $1.5 \,\text{V}$ and Vrf = $0.35 \,\text{V}$.

Fig. 8A presents the waveforms of measured currents I_{u_i} and I_{v_i} , where the bias currents were set at $I_{\tau} = 100$ nA and $I_s = 200$ nA. Fig. 8B shows a closed (I_{ui}, I_{v_i}) phase plane portrait. These results show that the circuit generates stable oscillation that is qualitatively the same as that of simulation results. However, we found the influence of device mismatch on the oscillation, such as asymmetry in the waveforms of the currents and a distortion in the phase plane portrait.

5. Device mismatch compensation of exploiting physical feedback

Here, we propose a compensation for the influence of the device mismatch of the piecewise linear neuromorphic oscillator exploiting sensory feedback from a coupled physical system.

5.1. Feedback control loop including nonlinear oscillator and physical system

In the following, we consider a feedback control loop including a nonlinear oscillator and a physical system as shown in Fig. 9. Williamson has investigated entrainment properties of such a loop, and applied the entrainment properties for controlling rhythmic arm movements of a humanoid [33]. As a result of the entrainment of the feedback control loop, the amplitude and frequency of oscillation of both the nonlinear oscillator and the physical system are modulated. The necessary condition of the stable oscillation is given by the following equation:

$$|C(s)P(s)| \ge 1,\tag{12}$$

where C(s) and P(s) represent transfer functions of the nonlinear oscillator and the physical system, respectively.



Fig. 9. Block diagram of feedback control loop including nonlinear oscillator (OSC) and physical system.



Fig. 8. Experimental results: (A) waveforms of currents and (B) phase plane portrait of the piecewise linear neuromorphic oscillator.



Fig. 7. Micrograph of a fabricated chip.

5.2. Reducing influence of device mismatch through physical feedback

We here consider exploiting the transfer characteristics of the feedback control loop to reduce the influence of the device mismatch in the piecewise linear neuromorphic oscillator. The transfer function of the feedback control loop T(s) is described by the following equation:

$$T(s) = \frac{C(s)P(s)}{1 + C(s)P(s)},$$
(13)

where C(s)P(s) is the input loop transfer function. When we regard C(s) as the transfer function of the piecewise linear neuromorphic oscillator, the device deviation of the neuromorphic oscillator can be regarded as the deviation of C(s), namely, $\delta C(s)$. When we differentiate T(s) with respect to s, then we obtain the following equations:

$$\frac{\partial T(s)}{\partial C(s)} = \frac{C(s)}{(1+C(s)P(s))^2} = \frac{1}{1+C(s)P(s)} \cdot \frac{T(s)}{C(s)},$$
(14)

that yield

$$\frac{\delta T(s)}{T(s)} = \frac{1}{1 + C(s)P(s)} \cdot \frac{\delta C(s)}{C(s)} = S(s) \cdot \frac{\delta C(s)}{C(s)},\tag{15}$$

where we assumed that P(s) is invariant. Here, $\delta T(s)$ represents the deviation of T(s), and S(s) the sensitivity function. For reducing the influence of the device deviation of the piecewise linear neuromorphic oscillator $\delta C(s)$ on the transfer characteristics of the entire system, the following condition:

$$|S(s)| = |1 + C(s)P(s)|^{-1} \ll 1$$
(16)

should be satisfied.

5.3. Transfer characteristics of physical system

We here consider a joint actuator with single degree-offreedom (DOF) as a physical system. The dynamics of the joint actuator is given as follows:

$$M\ddot{\theta} + k\dot{\theta} + c\theta = \tau, \tag{17}$$

where θ is the joint angle, *M* the moment of inertia of the joint actuator, *k* the stiffness parameter, and *c* the damping parameter. We introduce a simple proportional differential (PD) controller generating the driving force τ :

$$\tau = K_{\rm P}(\hat{\theta} - \theta) - K_{\rm D}\dot{\theta},\tag{18}$$

where $\hat{\theta}$ is the equilibrium angle. We assumed that θ and $\dot{\theta}$ can be measured quite accurately. The parameters $K_{\rm P}$ and $K_{\rm D}$ represent the proportional and differential parameters, respectively. The physical system including the PD controller (Fig. 10) is regarded as a standard second-order system, then the transfer function of the system P(s) is

Table 1	
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Parameters	Without feedback	With feedback
ξ	1.0	1.0
ω_n (rad/s)	10	10
G (rad/nA)	0.05	0.02
ζ (nA/rad)	0	0
η (nA s/rad)	500	0



Fig. 10. Configuration of control system including feedback loop D.



Fig. 11. Transfer characteristics of the piecewise linear neuromorphic oscillator.



Fig. 12. Physical feedback compensation.

given as follows:

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$$P(s) = \frac{\omega_{\rm n}^2}{s^2 + 2\xi\omega_{\rm n}s + \omega_{\rm n}^2}, \quad \xi = \frac{K_{\rm D} + c}{2\sqrt{M(K_{\rm P} + k)}},$$
$$\omega_{\rm n} = \sqrt{\frac{K_{\rm P} + k}{M}}, \tag{19}$$

where ω_n is the natural angular frequency of the system, and ξ the damping parameter. Thus, P(s) can be regulated by tuning K_P and K_D .

5.4. Transfer characteristics of piecewise linear neuromorphic oscillator

The transfer function of the piecewise linear neuromorphic oscillator C(s) cannot to be derived analytically due to the nonlinearity. Thus, we estimated C(s) from simulation results, such as shown in Fig. 11. The input I(t) was given into the piecewise linear neuromorphic oscillator as follows:

$$\tau \frac{dI_{u_1}}{dt} = -I_{u_1} + f(I_s - \beta I_{v_1} - wI_{u_2} - I(t)),$$
(20)

$$\tau \frac{\mathrm{d}I_{v_1}}{\mathrm{d}t} = -I_{v_1} + f(I_{u_1}),\tag{21}$$

$$\tau \frac{\mathrm{d}I_{u_2}}{\mathrm{d}t} = -I_{u_2} + f(I_s - \beta I_{v_2} - wI_{u_1} + I(t)), \tag{22}$$

$$\tau \frac{\mathrm{d}I_{v_2}}{\mathrm{d}t} = -I_{v_2} + f(I_{u_2}) \tag{23}$$

and we defined the output as $I_{out} = I_{u_1} - I_{u_2}$. The transfer function C(s) is determined by the circuit parameters. In particular, the natural frequency is crucial for the entrainment between the neuromorphic oscillator and a physical system.

5.5. Monte-Carlo simulations

We simulated how the physical feedback reduces the influence of the device mismatch on the operation of the

feedback control loop through Monte-Carlo simulations using SPICE. We set circuit parameters for the neuromorphic oscillator as follows: $I_{\tau} = 10$ nA, $I_s = 500$ nA, C = 50 nF, $\beta = 5$ and w = 4. We tuned the parameters to determine the transfer function of the physical system including the PD controller for satisfying the conditions of (12) and (16), as shown in Table 1. The device deviation of the neuromorphic oscillator was defined as the threshold deviation of transistors: $\sigma(V_{\text{th}}) = 0.1\%$.

Fig. 12 shows $(\theta, \dot{\theta})$ phase plane portraits of the joint actuator without and with feedback loop obtained by 100 trials. We here defined the equilibrium angle as $\hat{\theta}_i = G(I_{u_1} - I_{u_2})$, where G is a transformation coefficient, and the physical feedback as $S = \zeta \theta + \eta \dot{\theta}$. These results confirmed that the physical feedback could reduce the influence of the device deviation of the neuromorphic oscillator on the behavior of the joint actuator.

6. Conclusion

We have proposed an analog current-mode subthreshold CMOS circuit for the piecewise linear neuromorphic oscillator model. Our circuit consists of four current-mode low-pass filters and several current mirrors that operate in their subthreshold region under the low-power supply voltages. As a result, low power consumption can be achieved. We have confirmed that the circuit generates stable oscillation and the amplitude and frequency of the oscillation can be controlled by tuning the bias currents. These characteristics of our circuit are suitable as a building block for constructing a CPG-based controller. Furthermore, we have considered how to reduce the influence of the device mismatch on the circuit operation. The compensation method using the physical feedback that we proposed and was confirmed through Monte-Carlo simulations. Further consideration of applying our circuit and compensation method to implement micro robots remains as future work. In particular, low-power consumption of our chip and the physical feedback compensation method are attractive for driving underwater robots that move around unpredictable environments for a long time.

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